

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the subject application:

Listing of Claims

1. – 30. (Cancelled)

31. (Currently Amended) A method comprising:

accessing a packet template in a memory, the packet template having at least one static field; and

in response to an indication of an event, generating on an integrated circuit, without executing network layer software stacks for each protocol layer, a packet on an integrated circuit, the packet based on the packet template.

32. (Previously Presented) The method of claim 31, additionally comprising transmitting the packet to a communication controller for transmission over a shared medium.

33. (Previously Presented) The method of claim 31, additionally comprising generating the packet template in response to receiving data to be used as the packet template.

34. (Previously Presented) The method of claim 33, wherein the packet

template includes at least two protocol layers, each of the at least two protocol layers including at least two static fields.

35. (Previously Presented) The method of claim 34, wherein one of the at least two protocol layers includes an SNMP (Simple Network Management Protocol) layer.
36. (Previously Presented) The method of claim 35, wherein the generated packet includes a SNMP trap PDU (protocol data unit).
37. (Previously Presented) The method of claim 31, wherein the integrated circuit comprises an ASIC (application specific integrated circuit).
38. (Previously Presented) The method of claim 31, wherein said generating the packet comprises inserting one or more non-static data into the packet.
39. (Currently Amended) A method comprising:
- receiving data to be used to create a packet template;
- generating the packet template, the packet template including at least one static field;
- storing the packet template in a memory;
- receiving an indication of an event; and
- ~~substantially independently of a processor, generating[.] on an integrated circuit, without executing network layer software stacks for each protocol layer, a packet based on the packet template.~~

40. (Previously Presented) The method of claim 39, wherein the packet template includes at least two protocol layers, each of the at least two protocol layers including at least two static fields.
41. (Previously Presented) The method of claim 40, wherein one of the at least two protocol layers includes an SNMP (Simple Network Management Protocol) layer.
42. (Previously Presented) The method of claim 41, wherein the generated packet includes a SNMP trap PDU (protocol data unit).
43. (Previously Presented) The method of claim 39, wherein the integrated circuit comprises an ASIC (application specific integrated circuit).
44. (Previously Presented) The method of claim 39, wherein said generating the packet comprises inserting one or more non-static data into the packet.
45. (Currently Amended) An apparatus comprising:

an integrated circuit having:

a memory to store at least one packet template, the at least one packet template having at least one static field; and

a packet generator to generate on the integrated circuit, without executing network layer software stacks for each protocol layer, and in response to receiving an indication of an event,
~~on an integrated circuit, and in response to an indication of an event,~~ a packet based on one of the at least one packet

template.

46. (Previously Presented) The apparatus of claim 45, additionally comprising an event processor to receive an indication of one or more events, and to notify the packet generator of the one or more events.
47. (Previously Presented) The apparatus of claim 46, wherein one of the one or more events includes a software-generated event from a CPU (central processing unit).
48. (Previously Presented) The apparatus of claim 46, wherein one of the one or more events includes an external event.
49. (Previously Presented) The apparatus of claim 48, wherein the external event is polled from a device.
50. (Previously Presented) The apparatus of claim 46, wherein:

the event processor additionally sends an event code and event data to the packet generator; and

the packet generator generates a packet based on one of the at least one packet templates by:

accessing the packet template in the memory, the packet template including a partial checksum;

storing the event code and the event data in the packet template;

calculating a complete checksum based on the partial checksum,

and based on the at least one static field;

storing the complete checksum in the packet template; and

transmitting the packet template to a communication controller for
transmission over a shared medium.

51. (Previously Presented) The apparatus of claim 45, additionally including a bus control module to receive at least one packet template from a CPU (central processing unit).
52. (Previously Presented) The apparatus of claim 51, wherein the bus control module additionally receives a partial checksum from the CPU.
53. (Previously Presented) The apparatus of claim 45, wherein the packet comprises an SNMP (Simple Network Management Protocol) trap PDU (protocol data unit).
54. (Previously Presented) The apparatus of claim 53, wherein the SNMP trap PDU comprises a UDP (User Datagram Protocol) packet portion.
55. (Previously Presented) The apparatus of claim 54, wherein the complete checksum is stored in the UDP packet portion.
56. (Currently Amended) A system comprising:

a network interface card having a communications controller; and

an integrated circuit coupled to the network interface card, the integrated circuit including:

a memory to store at least one packet template, the at least one packet template having at least one static field; and

a packet generator to generate[,] on the ~~an~~ integrated circuit, without executing network layer software stacks for each protocol layer, and in response to an indication of an event, a packet based on one of the at least one packet template.

57. (Previously Presented) The system of claim 56, additionally comprising an event processor to receive an indication of one or more events, and to notify the packet generator of the one or more events.

58. (Previously Presented) The system of claim 57, wherein:

the event processor additionally sends an event code and event data to the packet generator; and

the packet generator generates a packet based on one of the at least one packet templates by:

accessing the packet template in the memory, the packet template including a partial checksum;

storing the event code and the event data in the packet template;

calculating a complete checksum based on the partial checksum, and based on the at least one static field;

storing the complete checksum in the packet template; and

transmitting the packet template to a communication controller for
transmission over a shared medium.

59. (Previously Presented) The system of claim 58, wherein the packet comprises an SNMP (Simple Network Management Protocol) trap PDU (protocol data unit).

60. (Previously Presented) The system of claim 59, wherein the SNMP trap PDU comprises a UDP (User Datagram Protocol) packet portion.

61. (Currently Amended) A method comprising:

in response to an indication of an event, generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template; and
transmitting the packet to a communication controller for transmission over a shared medium.

62. (Previously Presented) The method of claim 61, wherein said indication of an event comprises receiving an event code and event data.

63. (Previously Presented) The method of claim 62, additionally comprising storing the event code and event data in the packet template.

64. (Previously Presented) The method of claim 63, additionally comprising storing a timestamp and sequence number in the packet template.

65. (Previously Presented) The method of claim 64, additionally comprising

calculating a complete checksum and storing the complete checksum in the packet template.

66. (Previously Presented) The method of claim 61, additionally comprising determining one or more static fields of the packet template.
67. (Previously Presented) The method of claim 61, wherein the indication of an event is generated in response to polling a device that does not have a normal status.
68. (New) The method of claim 61, wherein said generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template is performed substantially independently of a central processing unit.
69. (New) The method of claim 56, wherein said generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template is performed substantially independently of a central processing unit.
70. (New) The method of claim 45, wherein said generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template is performed substantially independently of a central processing unit.
71. (New) The method of claim 39, wherein said generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template is performed substantially independently of a central processing unit.

72. (New) The method of claim 31, wherein said generating a packet on an integrated circuit, without executing network layer software stacks for each protocol layer, the packet based on a packet template is performed substantially independently of a central processing unit.